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ABSTRACT

An integrated circuit for use in implementing a switching voltage regulator, the integrated circuit including a power switching transistor, driver circuitry and control circuitry, which is operable in a normal feedback mode or an isolated flyback mode. The integrated circuit includes shutdown circuitry for placing the regulator in a micro-power sleep mode, and can be packaged in a five-pin conventional power transistor package. The terminals of the integrated circuit regulator perform multiple functions. A compensation terminal is used for frequency compensation, current limiting, soft-start operation and shutdown. A feedback terminal is used as a feedback input when the integrated circuit is in feedback mode, and as a logic pin to program the regulator for isolated flyback operation. The feedback terminal is also used to trim the flyback reference voltage.